VSLI & DIGITAL SYSTEM DESIGN CLUB

Faculty Coordinators:

Dr. Anupama Mehra (amehra@amity.edu)Dr. Pradeep Kumar (pkumar4@amity.edu)Dr. Sunny Anand (sanand5@amity.edu)

Student Coordinators:

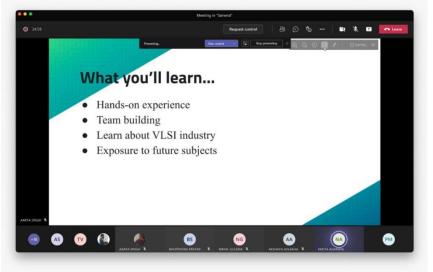
Name of President : Rohan Srinivasan Email ID of President rohan.srinivasan00@gmail.com

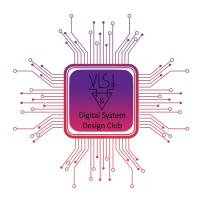
Name of Vice President : Khushi Gupta Email ID of Vice President : khushigupta1113@gmail.com

Name of Secretary : Aarya Singh Email ID of Secretary: aarya.singh2016@gmail.com

Events organized for the academic year 2021-2022:

• UNMASKING VLSI: (2nd September 2021, 2:30 PM onwards on MS Teams) An introductory event was held where seniors briefed about the VLSI club. Interesting quizzes were held. It was an interesting session overall. The main objective was to give a brief description about the scope in VLSI. It shows the gateway of VLSI industry to know about digital electronics and circuit simulation.

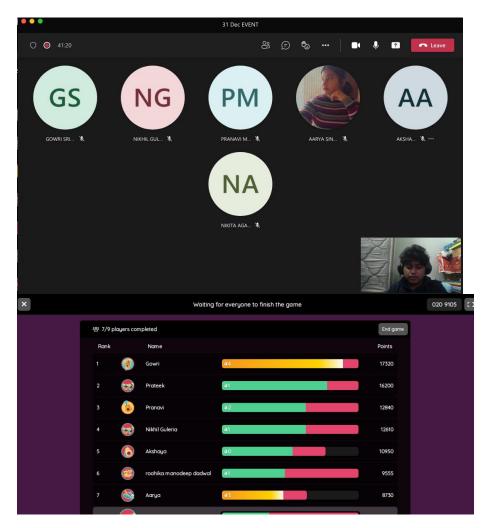




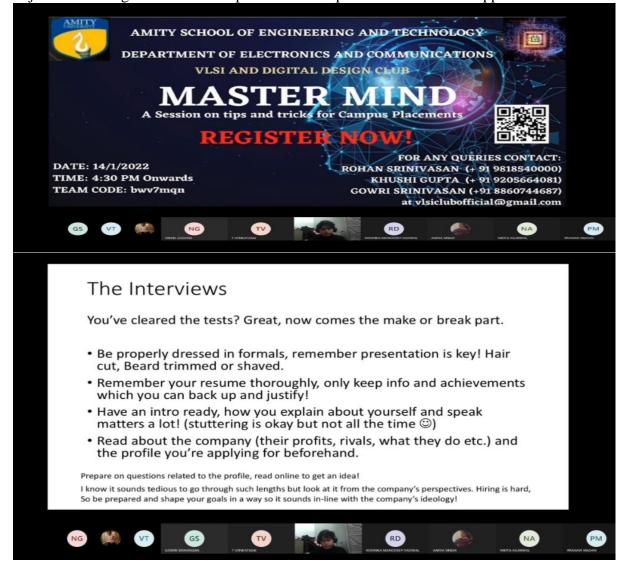


• DIGIPEDIA: (31st December 2021, 4PM onwards on MS Teams)

In the event a quiz was conducted on the basics of electronics. Questions related to basics of electronic circuits were asked in an MCQ format on an online quizzing platform. The quiz provided an exuberating experience to the participants and inculcated friendly competition among them.



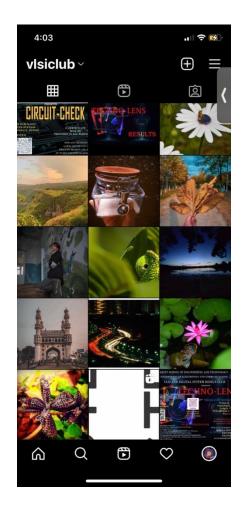
 MASTER MIND (14th January 2022, 4:30PM onwards on MS Teams) In the event the seniors briefed about the tips and tricks for campus placements. The 4th year students shared their experiences regarding how to crack good companies and prepare for campus interview. It was an interesting session overall. The main objective was to give a brief description about the placement and future opportunities.



• TECHNO-LENS (26th January 2022 on Instagram)

An open photography competition held for school and college students from all over the country. Participation and assessment all happened on the Official VLSI club Instagram page @vlsiclub.





• CIRCUIT-CHECK (17th March 2022 on MS Teams)

An interesting competition based on debugging circuit diagrams. Students were provided with circuit diagram, each having components missing or mis-placed, and ut was up-to them to figure out what was wrong!

