As part of our Higher Education Program, Mentor Graphics Noida (a Corporate R&D Center of Mentor Graphics Corporation) conducted training on "Verification of Electronic Design and Systems using SystemVerilog" in 2009 and was well received by the 30 selected students. The 8 week training was attended by 30 selected students from colleges in Delhi NCR and North India. The training gave them an insight of the EDA Industry and helped them get a headstart in their career. Some testimonials of the students who attended the training program and other statistics are available at www.mentor.com/india.

Continuing with our initiative to help students become proficient in leading edge EDA tools, Mentor Graphics Noida, is pleased to announce the 2nd “Verification of Electronic Design and Systems using SystemVerilog” training program in 2010. This training program is available for students who have graduated from colleges in Delhi NCR and North India and who have not yet found placement. The program will be for eight weeks beginning August 16th – October 10, 2010 from 10am to 6pm (Monday to Friday). Industry professionals and Experts will deliver this program using SystemVerilog Hardware Verification Language constructs and OVM methodology. The program will include extensive labs using Mentor Tools and will also include some projects.

The Key Modules of the program are:

- Introduction to Logic Design and Verification (Block level and System Level)
- Introduction to SystemVerilog as a Verification Language
- Introduction to SystemVerilog Open Verification Methodology
- Concepts of Verification Management and Coverage using Questa Verification environment

This program is available free of cost. An application form for this program is available at (http://www.mentor.com/india/eduservices/india-hep-course). Interested students should fill up this form and submit it online latest by July 23, 2010. Short-listed applicants will be called for a written (aptitude/analytical) test in our office on July 31, 2010. A total of 30 students will be selected for this program. Selected applicants will be informed by August 6, 2010.

Mentor Graphics Corporation (www.mentor.com) is a world leader in Electronic Design Automation (EDA) Tools business having its
headquarters in USA. Mentor Graphics has been present in India since 1997 and has three offices in Noida, Hyderabad and Bangalore.

Best Wishes

Prof. (Dr.) Ajay Rana
Director